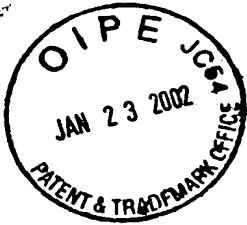


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT

APPLICANT: Marcel Heerman
SERIAL NO.: 09/997,345 GROUP ART UNIT: 2811
FILED: November 29, 2001
INVENTION: "AN INTERMEDIATE BASE FOR A SEMICONDUCTOR
MODULE, A SEMICONDUCTOR MODULE USING
THE INTERMEDIATE BASE AND A METHOD FOR
PRODUCING THE INTERMEDIATE BASE"

Assistant Commissioner for Patents
Washington, D.C. 20231

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TC 2800 MAIL ROOM

SIR:

In accordance with the provisions of 37 CFR 1.56, 1.97 and 1.98, it is requested that a citation and examination of the references listed on the attached Form PTO-1449 be made during the course of the examination of the above-identified application.

REMARKS

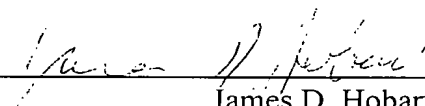
On pages 2 and 3 of the present application, in the Background of the Invention, U.S. Patent Nos. 5,069,626; 5,929,516 and 6,249,048 along with European 0 782 765 and WO 89/00346 were cited. Copies of these five references are attached herewith. It is submitted that the specification distinguishes the claims of the present application over the teachings of these five references.

It is noted that on page 5, pages 15-18 from Journal Advancing Microelectronics, July/August 1998 was cited. The undersigned attorney does not have a copy of this reference at

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the present time, but have requested a copy and will forward a copy to the Patent Office in a Supplemental Information Disclosure Statement as soon as it is received.

Respectfully submitted,



James D. Hobart
SCHIFF HARDIN & WAITE
Patent Department
6600 Sears Tower
233 South Wacker Drive
Chicago, Illinois 60606
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Customer Number 26574

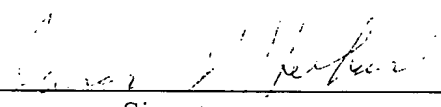
(Reg. No. 24,149)

DATED: January 2, 2002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on January 2, 2002.

James D. Hobart
Name of Applicant's Attorney



Signature

January 2, 2002

Date